

Appl. No. : 09/277,335
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application programs themselves, data used by application programs, or any other information that the host computing system stores to the data storage devices 14 of the system. As will be further explained below with reference to Figures 2 and 3, the encryption/decryption logic may in some embodiments be configurable to perform the encryption and decryption on a selectable subset of the data storage devices if desired by a user of the system.

Please replace the paragraph beginning at page 7, line 25 with the following rewritten paragraph:

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The processor 36 interfaces with a host bus 44 which also interfaces with a bridge circuit 46. The bridge circuit 46 routes data from the host bus 44 to a PCI bus 48. The PCI bus 48 provides a data source to a logic circuit 50 which is provided in the data path between the PCI bus 48 and IDE bus 52 and floppy drive control bus 54 which interface directly with the respective data storage devices 38, 40, and 46. The PCI bus 48 may also receive data from I/O devices 56 via a PCI to ISA bridge circuit 58.

If the Examiner has any questions which may be answered by telephone, he is invited to call the undersigned directly.

Respectfully submitted,

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